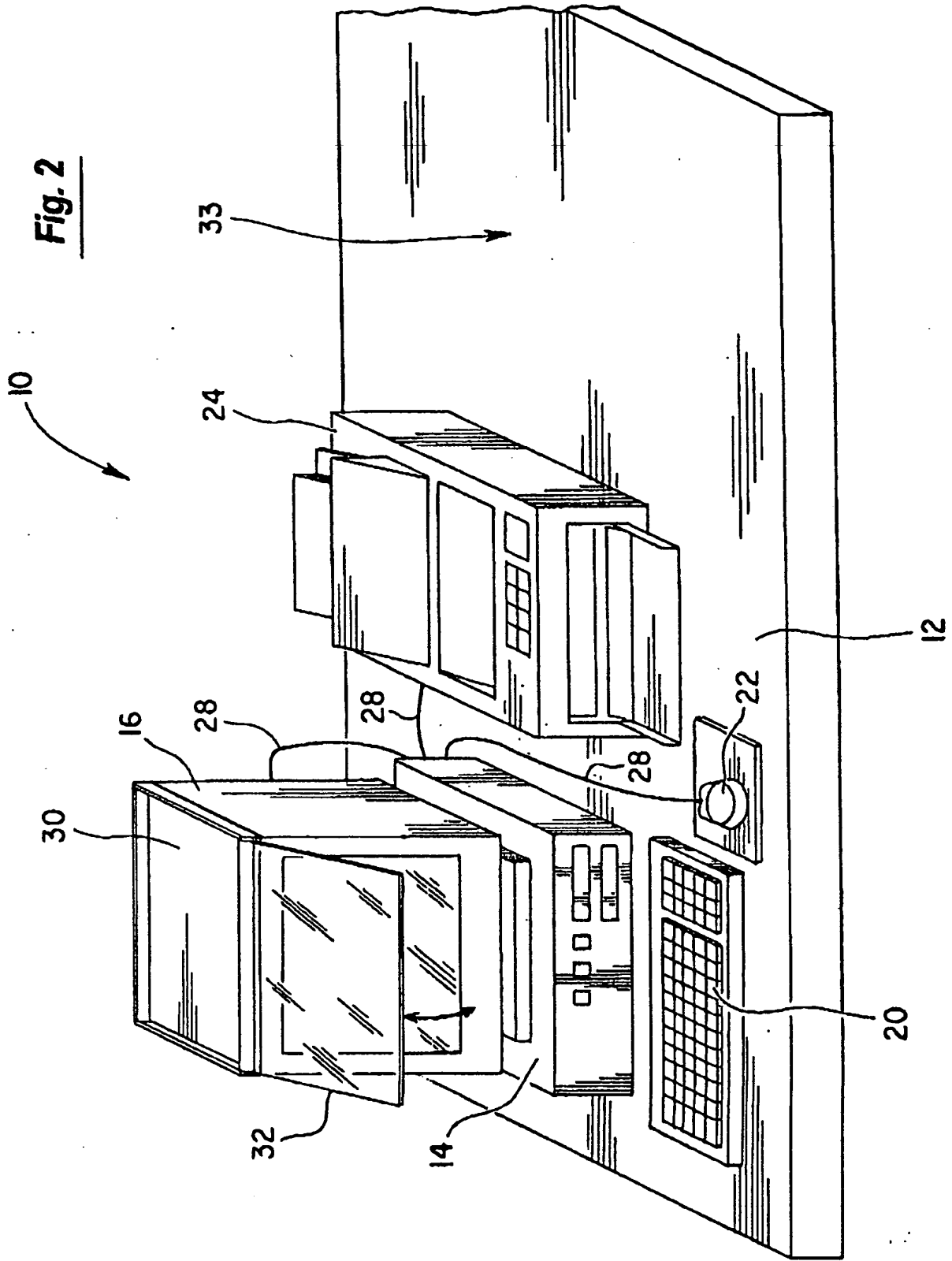
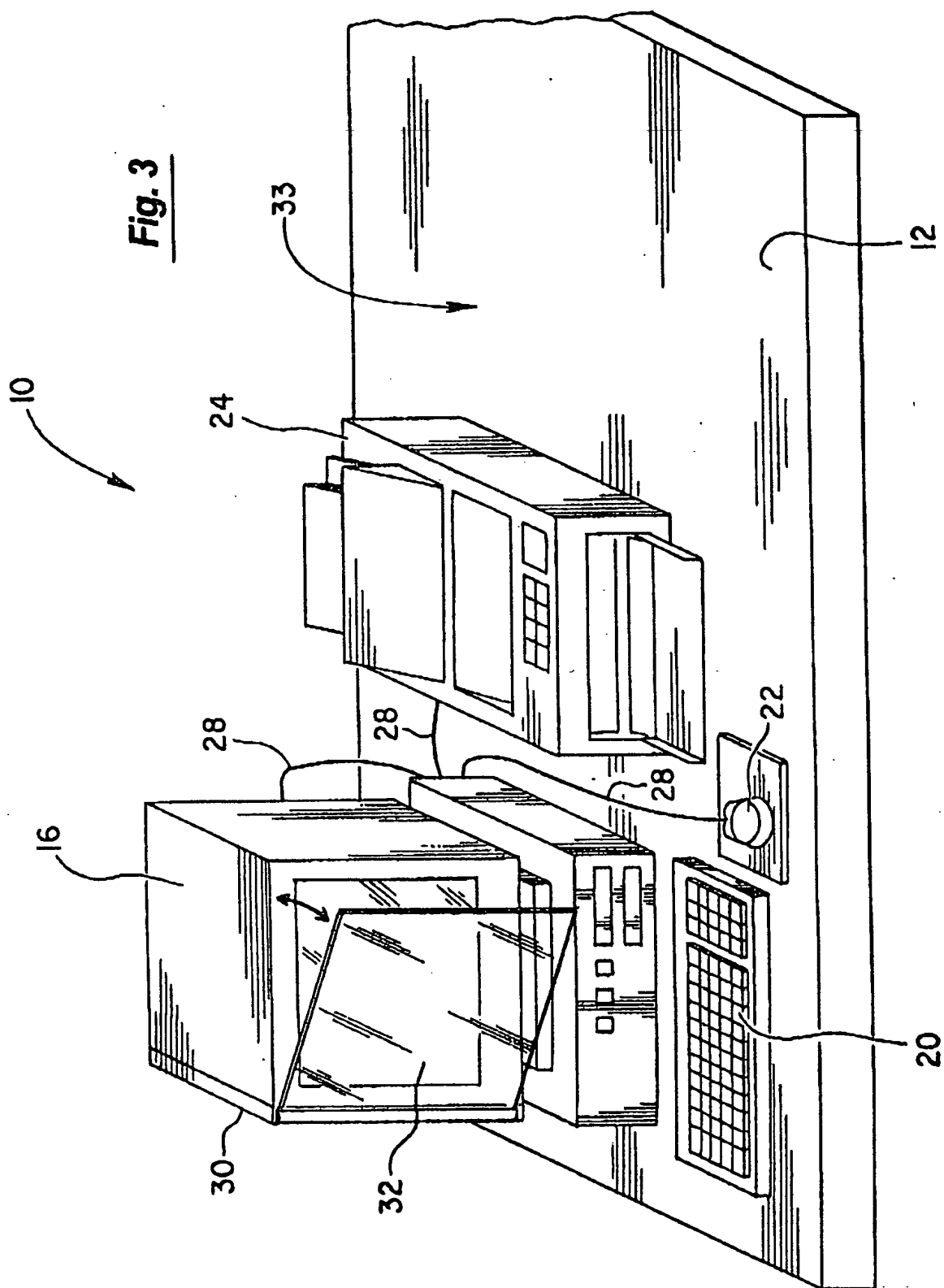


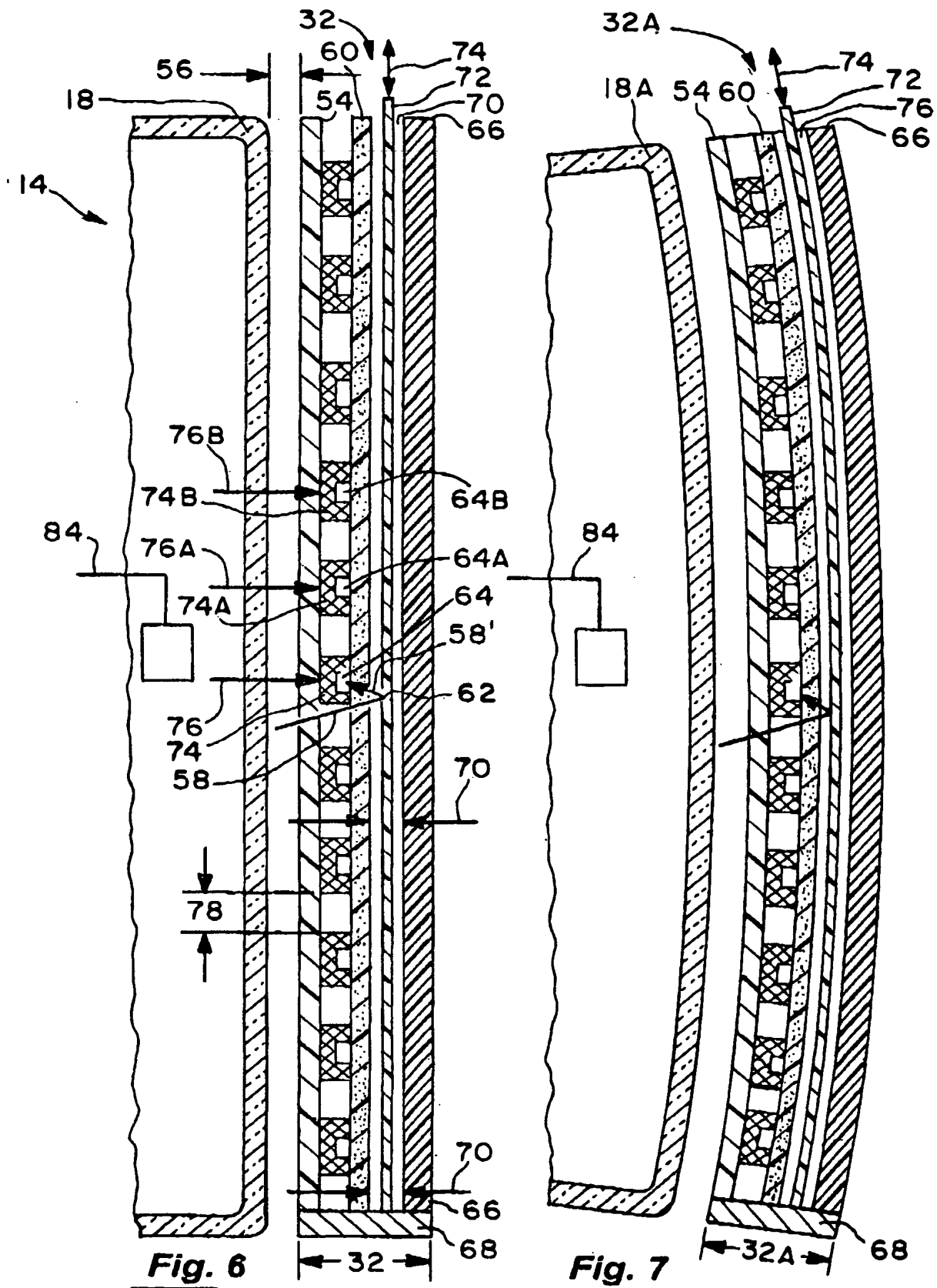
Fig. 1

Fig. 2









The diagram illustrates a computer system architecture. On the left, a vertical assembly consists of several stacked components labeled 76, 74, 77, 74A, and 56. These are connected via a bus or cable labeled 84 to a central processing unit labeled 16. To the right of the central unit is another vertical stack of components labeled 60, 72, 66, 64L, 64, 58', and 62. This stack is also connected to the central unit 16. At the top right, there is a keyboard labeled 20 and a monitor or display unit labeled 14. The entire system is powered by a power supply unit labeled 80, which is connected to the central unit 16 and the other components.

Fig. 8

